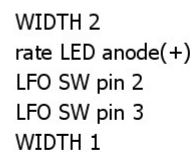


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RATE 1&2
RATE 3



LEVEL 3
BLEND 1
BLEND 3

+9V
GND

OCTAVE SW pin 2
OCTAVE SW pin 1

IC1: NE555
IC2: TL072
IC3: CD4053
IC4: CD4069
Q1: 2N3904

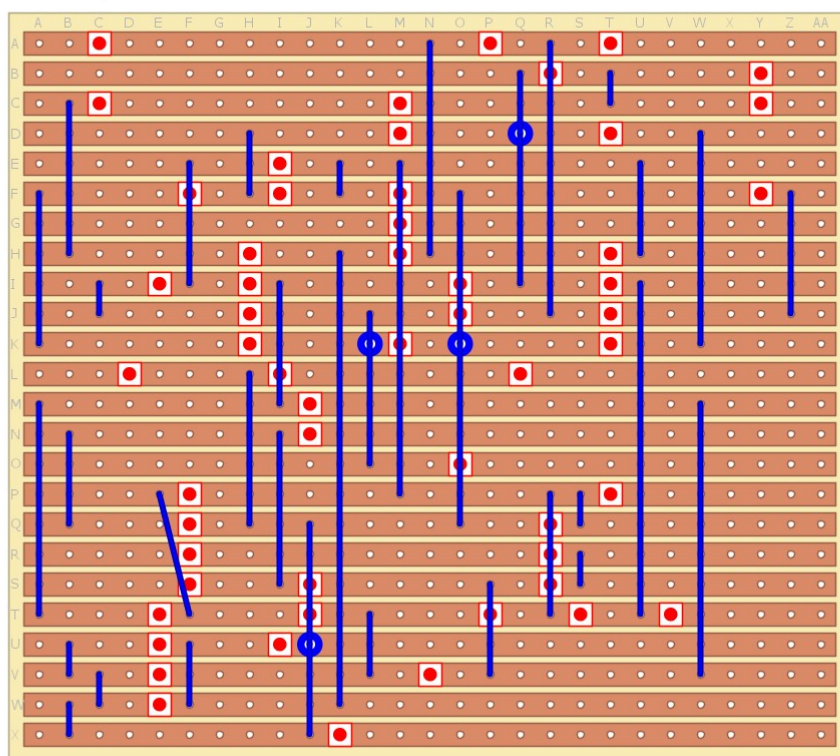
WIDTH: B100K
BLEND: B50K
RATE: C50K
LEVEL: A100K
VR1: 200K trimpot

WIDTH 3 to GND
BLEND 2 to GND
LEVEL 1 to GND
LEVEL 2 to Output
Rate LED cathode to GND

LFO SWITCH: SPDT on/on
OCTAVE SWITCH: SPDT on/on

* Current Limiting Resistor for the rate LED
I recommend using a clear superbright LED with a 15K resistor
Lower values CLR can interfere with the LFO

27 x24, 39 links and 55 cuts



● = double links